



FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
ARC.015RC1APPLICATION NO.  
09/808,469INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT

(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT  
Peter WarnesFILING DATE  
March 14, 2001GROUP  
2122 2191

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
MX	1	5,493,508	2/20/96	Dangelo, et al.			
MX	2	5,535,331	7/9/96	Swoboda et al.			
MX	3	5,544,067	8/6/96	Rostoker, et al.			
MX	4	5,555,201	9/10/96	Dangelo, et al.			
MX	5	5,801,958	9/1/98	Dangelo et al.			
MX	6	5,867,399	02/1999	Rostoker, et al.			
MX	7	6,173,434	01/2001	Wirthlin, et al.			
MX	8	6,324,678	11/2001	Dangelo, et al.			
MX	9	6,378,123	04/2002	Dupenloup			
MX	10	6,421,818	07/2002	Dupenloup, et al.			
MX	11	6,477,697 B1	11/5/02	Killian, et al.			
MX	12	6,760,888	7/8/04	Killian, et al.			
MY	13	6,862,563	03/01/05	Hakewill, et al.			

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
MX	14	2 308 470	6/97	GB				
MX	15	WO 97 13209 A	04/1997	WIPO				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
MX	16	Elms, A., "Tuning a Customisable RISC Core for DSP," Electronic Product Design, Sept. 1997, Vol. 18, No. 9, pages 19-20, 22, XP000909039
MX	17	Tensilica, Inc; Xtensa Instruction Set Architecture (ISA) Reference Manual Revision 1.0. (First 25 pages)
MX	18	Tensilica, Inc. Instruction Extension Language (TIE) Reference Manual Revision 1.3.
MX	19	Compton et al., "Configurable Computing: A Survey of Systems and Software," Technical Report, Northwestern University, Dept. of ECE, 1999.
MX	20	Yang, Jin-Hyuk, et al., "MetaCore: A Configurable & Instruction-Level Extensible DSP Core," Proceedings of the ASP-DAC '98 Asian and South Pacific Design Automation Conference 1998, pages 325-328, XP-002137268

EXAMINER

DATE CONSIDERED

6.21.05

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